

## PROCESSOR REGISTERS HAVING STATE INFORMATION

### **Abstract of the Disclosure**

5           Methods and apparatus are provided for implementing an efficient processor  
having state information included in each register. A processor has registers  
configured to hold both data and state information, such as carry and overflow  
information. State information and data can be read and written in the same  
operation. Holding state information along with data in the same register can provide  
10 a variety of benefits, particularly in the context of multithreaded programmable chips.